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**PATENT NUMBER and
ISSUE DATE**

U.S. UTILITY Patent Application

APPL NUM 10022222	FILING DATE 12/20/2001	CLASS 364	SUBCLASS 174	GAU 264	EXAMINER 3827
**APPLICANTS: Yazaki Yoshitarou; Yokochi Tomohiro; Kondo Koji; Harada Toshika zu; Shiraishi Yoshihiko;					
**CONTINUING DATA VERIFIED:					
** FOREIGN APPLICATIONS VERIFIED: JAPAN 2000-395601 12/26/2000 JAPAN 2001-94176 03/28/2001 JAPAN 2001-204024 07/04/2001					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no				ATTORNEY DOCKET NO 01-241	
Verified and Acknowledged Examiners's initials					
TITLE : Printed wiring board and method for manufacturing printed wiring board					

U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Assistant Examiner	Total Claims Print Claim for O.G. 1E
ISSUE FEE		Primary Examiner	DRAWING
Amount Due	Date Paid		Sheets Drwg. Figs. Drwg. Print Fig.
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE Application Examiner	
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